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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:) Group Art Unit: 2814
KOMIYAMA et al.)
Serial No. 09/776,528) Examiner: Ha, Nathan W.
Filed: February 4, 2001)
For: METHODS FOR MANUFACTURING)
SEMICONDUCTOR CHIPS, METHODS FOR)
MANUFACTURING SEMICONDUCTOR)
DEVICES, SEMICONDUCTOR CHIPS,)
SEMICONDUCTOR DEVICES, CONNECTION)
SUBSTRATES AND ELECTRONIC DEVICES)

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sirs:

In response to the Election/Restriction Requirement dated September 18, 2002, the response being due by October 18, 2002, please enter and consider the following.

Applicant hereby elects to prosecute Group I, claims 1-8 and 22-23, drawn to methods.

It is believed that no fees are due. However, if a fee is necessary for consideration of this paper, please charge the fee to Deposit Account 50-0585. A copy of this paper is enclosed.

Respectfully submitted,

Alan S. Raynes

Alan S. Raynes

Reg. No. 39,809

KONRAD RAYNES VICTOR & MANN, LLP
315 South Beverly Drive, Suite 210
Beverly Hills, CA 90212
Customer No. 24033

Dated: October 18, 2002

(310) 556-7983 (tele general)

(310) 871-8448 (tele direct)

(310) 556-7984 (fax)

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on October 18, 2002.

Alan S. Raynes
Alan S. Raynes

Oct. 18, 2002
(Date)